

# QUESTION-ANSWER FOR ORAL

Prof. Anand Ghara -  
Assistant Professor  
Computer Dept.  
PVG COE, Nasik  
8087777708

Subject :- System Programming & Operating System (SPOS)  
Subject Code :- 310257 (SPOS LAB) TW - 25 mark  
Total Practical :- 14 PR - 50 mark

## UNIT - I

1) System Programming :-  
- it is the activity of writing and maintaining system sw.

2) System sw :-  
- it is computer sw designed to operate and control the comp h/w & provide platform for running appli sw.  
e.g. OS, utility sw, device driver, compiler.

3) Application sw :-  
- it is any program or group of program that is designed for end user.  
e.g. d/b program, word processor, web browser.

4) Software Development tools :-  
- 1) Editor 2) prog environment 3) Debug monitor 4) User Interface.

5) Types of text editor :-  
- 1) Line editor 2) word processor 3) Screen editor 4) Structure editors.

6) Basic function of loader :-  
- 1) Allocation 2) Relocation 3) Linking 4) loading

7) Assembler :-  
- it converts assembly code into m/c code.  
Syntax :- Label, Mnemonics, operand1, operand2, opcode  
e.g. tasm, masm, Nasm etc

8) Macro-processor :-  
- it allows sequence of src lang. code to be defined once & then referred to by its name.  
Syntax :- macro name, macro body  
End of macro def

9) Compiler :-  
- it convert high level lang into low level lang. prog.  
e.g. C, C++, GCC, java compiler.

10) Types of Compiler :-  
- 1) Cross compiler 2) Bootstrap compiler 3) Incremental 4) Native compiler.

11) Language Processor :-  
- it is sw which bridges a specification over exe. gap.

12) Types of Lang. processor :-  
- 1) Lang. translator 2) preprocessor 3) De translator 4) Lang. migrator

13) Interpreter :-  
- It is a program, which scan prog. line by line & generate ICG code.  
e.g. VB interpreter, java interpreter.

14) Compare Compiler Vs Interpreter :-  
1) compiler scan whole code 2) scan line by line  
3) code is optimised 4) No optimization  
5) e.g. C, C++ 6) VB, java

15) Von neuman Architecture :-  
- 1) I/P unit 2) ALU 3) memory unit 4) O/P unit 5) control unit 6) AC 7) DR 8) PC 9) IR 10) MAR.

16) Assembly lang. statement :-  
- 1) imperative statement (IS) - MOVER Arg, X  
2) Declarative statement (DS) - X DS L  
3) Assembler Directive (AD) - START, END

Prepared By :- Prof. Anand Ghara



### 17) Assembler Directive:

- it gives direction to an assembler which task is to be performed

e.g. START & END.

### 18) Literal and Constant:

- literal is an immediate operand seems in statement.

- e.g.  $x = 4 + 5$ ; - 5 is known as literal.

- constant:  $Z = 5$ ; 5 is constant.

### 19) Literal vs Constant

1) literal can't be changed 2) constant can be changed

e.g.  $x = 4 + 5$       2)  $Z = 5$

3) it is safe      3) it is not safe.

4) part of instr<sup>n</sup>      5) not part of instr<sup>n</sup>

### 20) Types of Assembler:

1) Load & go assembler.

2) pass-1 assembler. 2) pass-2 assembler.

### 21) Forward References:

- values of variable is stored in forward instr<sup>n</sup>

e.g. `START 100`  
`MOVER Areg, X`  
`X DC 1`

Here value of X is reference to variable X.

NOTE: Backward reference is vice versa.

### 22) Advanced Assembler Directive:

1) ORIGIN      2) EQU      3) LORG <sup>literal origin</sup>

### 23) Backpatching in Pass-1 assembler.

- Problem of forward reference is known as backpatching. it is solved by Pass-2.

### 24) One pass Assembler:

- It scan src prog & generates

1) symbol table.      2) Pool table

3) literal table      4) ICG table.

(problem of Backpatching & forward references)

### 25) Two-pass assembler

- accept I/P from pass-1, resolve problem backpatching & forward references & finally generates m/c code.

### 26) Data structure for Pass-1

1) Machine Opcode Table (MOT)

2) Symbol Table (ST)

3) Literal Table (LT)

4) Pool Table (PT)

5) ICG Table (Intermediate Code)

### 27) Error reporting in assembler

1) Syntax error like missing comma

2) Invalid opcode

3) Undefined symbol

4) missing START OR END.

5) Symbol defined but not used.

### 28) Is literal processed in Pass-2?

- NO.

### 29) Undefined symbol are detected in Pass-1

- Yes.

### 30) Format of Intermediate Code

- Each mnemonic field is represented by (statement class & machine code)

NOTE: UNIT-1 most Q & A mentioned.

Even you should prepare all question & unit

- PASS-I Algorithm & Flowchart

- PASS-II Algorithm & Flowchart

- Block diag. for PASS-I & PASS-2

### 31) Practical 1 & 2 based on 1st unit

Prepare: How to run Assembler code.

Javac: Java Compiler.

- Prepare pass-1 & pass-2 examples &

Programs of file



## UNIT-II

### 1) MACRO VS SUBROUTINE.

- 1) macro is expanded 2) it can't be expanded
  - 2) exe. speed is more 2) less
  - 3) can't handle label 3) handle label.
- e.g. e.g.

### 2) Defining macro, calling, expansion.

- Calling - name of macro, argument

e.g. INCR X

- expansion - 

```
MOVEM Areg, X
ADD Breg, X
MEND
```

### 3) Types of parameter in MACRO

- 1) keyword parameter - INCR var=A, INCR=B
  - 2) positional 3) mixed parameter.
- e.g. INCR B var, STO B - both.

### 4) Nested Macro Call :-

- it is macro call within macro.

e.g. MACRO

```
ABC & ARG1
PQR & ARG2
MEND.
```

### 5) Advanced MACRO facility

- 1) AIF - Advance IF
- 2) AGO - Advance GO.

### 6) Issues related to Macro preprocessor :-

- 1) AIF 3) expansion time variable
- 2) AGO 4) sequencing symbol.

- Recognize macro def<sup>n</sup>

- Save macro def<sup>n</sup>

- Recognise MACRO call

- expand MACRO call

### 7) MACRO-PREPROCESSOR :-

- it take SRC prog containing macro def<sup>n</sup> & call

& translate into assembly lang prog. w/o any macro def<sup>n</sup>.

PROF. ANAND GHARU

### 8] Database/Data structure of PASS-1 macro-processor

- Macro Def<sup>n</sup> Table (MDT)

- Macro Name Table (MNT)

- Argument list Array (ALA)

- MNT pointer (MNTP)

- MDT pointer (MDTP)

### 9] Methods of handling Nested macro call :-

- Several level of expansion

- Recursion expansion

- Use of stack during expansion.

NOTE :-

Practical 3 & 4 depend on UNIT-2.

So, you should prepare all questions unit 2

Some other questions :-

\* Block diagram Pass 1 & 2 macro processor

\* Algo & Flowchart of Pass-1 & 2 macro.

\* Examples of macro processor. Pass 1 & 2

## LOADER

### 1] Loading schemes or Types of loader

- 1) Compile & Go 4) Subroutine linkage.
- 2) General loader 5) Relocating loader.
- 3) Absolute 6) Direct linking loader.

### 2] Overlay structure.

- it is part of prog. which have same load origin as some other part of prog.

## LINKER

### 1] Linker :-

it is prog. which links multiple object module together for exe. of prog.

### 2] Object Module :-

- it contains all info necessary to relocate & link different modules.

By - Prof Anand Ghauru



### 3] Static & Dynamic Link Libraries

- static linker takes object file produced by compiler. exe. file contain copy of <sup>exec</sup> subroutine
- static linker is fixed, can't be changed runtime.
- Dynamic linking:
  - it reference to an external module during runtime.
  - perform reloc<sup>d</sup> during runtime
  - changes can be possible in dynamic

### 4] Dynamic Link Libraries (DLL)

- DLL is microsoft imple<sup>d</sup> of shared library in window. file format for DLL is same as window EXE.
- A DLL can contain 1) code, data, resource. shared code is placed into a single, separate file, the prog that call file are connected to it at runtime, with OS performing linking.

### 5] Loading phases using java:

- 1) Loading
  - 2) Linking
  - 3) Initializing
- Bytecode verification  
class preparation  
Resolving

NOTE: UNIT-2 notes are completed.

you should read complete UNIT-2 with examples, Algorithm & flowchart.

PROF. ANAND GHARU

Prepared By: Prof. Anand Gharu

## UNIT-III

### LANGUAGE TRANSLATOR

#### 1) Token, pattern, lexemes & Error:

- Token - string of character in prog. e.g. identifier, keyword etc
- lexemes: is seq. of char in src prog. that is ~~pattern~~ matched by pattern for token. e.g. int xy = 5  
so xy is lexemes for token.
- pattern: set of rules to match token.
- Lexical error: error occurs when pattern not matched e.g. ; missing, rules not matched. etc.

#### 2] General model of compiler: (diagram) - phases of compiler:

- 1) lexical analyzer
- 2) syntax analyzer
- 3) semantic analysis
- 4) ICG
- 5) code optimization
- 6) code generation.

#### 3] Representation of ICG:

- 1) Three Address Code
- 2) Quadruple
- 3) Triple
- 4) postfix notation
- 5) Syntax tree
- 6) DAG Representation

#### 4] Code optimization techniques:

- 1) Compile time evaluation
- 2) Elimination of common sub-expre<sup>s</sup>
- 3) Dead Code Elimination.
- 4) freq<sup>y</sup> reduction
- 5) strength reduction.

#### 5] Design issues of code generator:

- 1) I/P to code generator
- 2) Target prog.
- 3) Memory management
- 4) Instr<sup>u</sup> selection
- 5) Register Allocation
- 6) choice of evaluation order
- 7) Approaches to code generation.

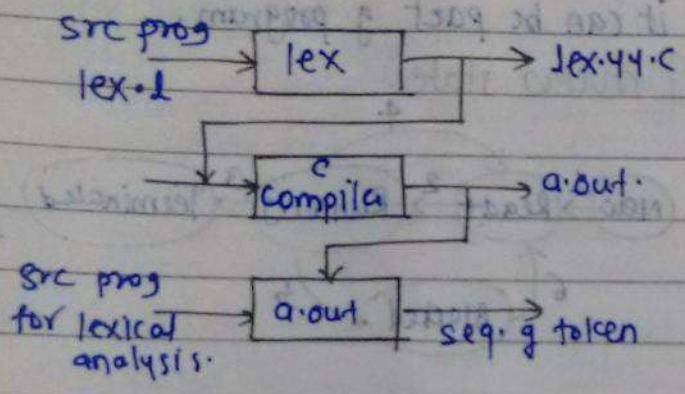


6] Software tools for Compiler Construction :-

- 1) Lex - analyzer generator
- 2) Yacc - parser generator

7] LEX :-

- it is a tool, which scan src prog & generates token as keyword, identifier etc.



\* Lex specification :-

- declaration % . . .
- translation rules % . . .
- user function.

\* function of LEX :-

- 1) yylex()
- 2) yytext()
- 3) yyteng()
- 4) yylval()
- 5) yywrap()
- 6) YYin()
- 7) YYout()

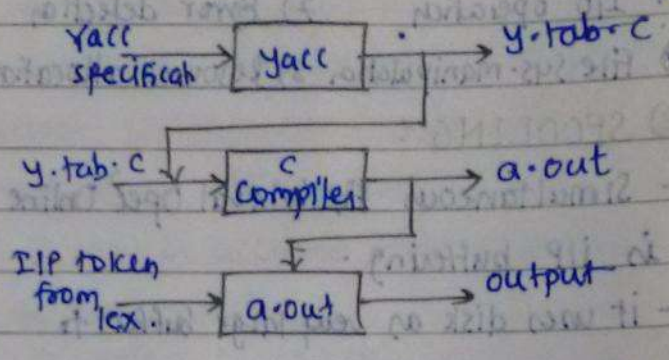
\* How to run Lex program

- Lex program .l  
 CC lex.yy.c  
 ./a.out

NOTE :- UNIT-3 notes are completed you should prepare all question of UNIT-3. you must prepare LEX & YACC program which we have studied.

8] YACC :-

- it stands for Yet Another Compiler Compiler
- it creates C-program for parser.
- Yacc accepts token generated by lex and match grammar & regular expressions in yacc prog with its specified rules.



\* YACC specification :-

- declaration % . . .
- % . . .
- translation rules % . . .
- % . . .

\* C-function :-

- The declaration section consist of token declaration & C-code by % { % }
- The context free grammar is placed in the rule section
- User function are added in last section
- Yacc generates file y.tab.c
- Yacc generates an header file y.tab.h for lex. this file contain integer value & it's each type of token.
- The y.tab.h file should be added in lex file function of YACC :-
- 1) yyparse()
- 2) yywrap()
- 3) yyerror()
- 4) yylex()
- 5) yylval()
- 6) yytext()



## OPERATING SYSTEM

1) Operating System :-

- It provides interface bet. user & h/w.

e.g. Windows 7, Ubuntu, Apple OS etc.

2) Function of Operating System :-

1) Prog. Development    5) Communication

2) Prog. execution    6) Resource sharing

3) I/O operation    7) Error detection

4) File Sys. manipulation    8) Resource allocation.

2) SPOOLING :-

- Simultaneous Peripheral Oper. Online in I/P buffering.

- It uses disk as very large buffer to store & read O/P file.

- Spooling allows CPU to overlap I/P of one job with O/P of other job.

3) Types of Operating system :-

1) Batch OS    4) N/w OS

2) Multiprogramming    5) Distributed OS

3) Real time OS    6) Time sharing OS.

4) Operating System Component :-

1) Processes    4) Signal

2) Files    5) Cmd Interpreter.

3) System call

4) System call :-

It provides interface to user program

with operating sys.

e.g. open(), close(), fork(), exit() etc.

5) Command Interpreter (Shell) :-

- It provides cmd. line interface.

It allow user to enter cmd. on cmd. line.

It interprete the cmd. entered by user

& executes it.

6) Types of Operating System Structure :-

1) Monolithic System

2) Layered System

3) Virtual machine

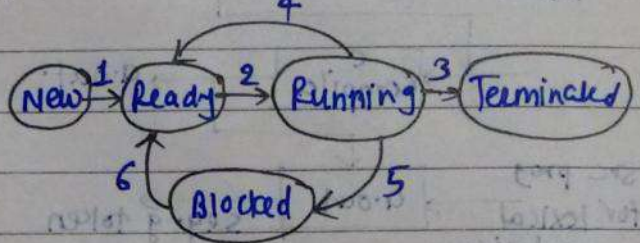
4) Client Server model.

7) process :-

- It is program while it is being executed

it can be part of program.

8) Process state :-



9) Process Control Block (PCB) :-

- PCB keeps track of all information

concerning a process.

i.e. process nos, process state, Prog. Counter, Registers, etc.

10) Thread :-

- Thread can be part of process

- OS support multiple thread execution.

- thread use memory of process.

so it is lightweight process/thread.

Two types - 1) Single threaded process

2) multithreaded processes

11) Process scheduling :-

It is set of policies & mechanism supported

by OS to control the order in which

work to be done is completed.

12) Scheduler :-

- It is OS program that select next job

to be admitted for exe.

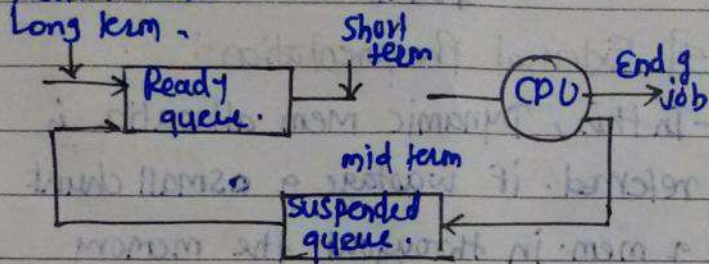


### 13] Performance criteria for Process scheduling:

- 1) CPU Utilization
- 2) Throughput
- 3) Turnaround time
- 4) Waiting time
- 5) Response time.

### 14] Types of Scheduler:

- 1) Long-term
- 2) medium term
- 3) short term



### 15] Scheduling methods

- 1) preemptive scheduling
- 2) Non-preemptive scheduling

### 16] Scheduling Algorithms (Types):

- 1) FCFS scheduling
- 2) SJF scheduling
- 3) RR scheduling
- 4) Priority scheduling

### 17] Interprocess Communication:

- it allows communicating processes to exchange data and info.

There are 2 methods of IPC

- 1) Through shared memory
- 2) Through Message Passing.

### 18] Race Condition:

- Race cond<sup>n</sup> is situation, where two or more processes are reading/writing some shared data and final result depend on who run precisely & when.

### 19] Critical Section:

In this, only single process can be run at a time in critical section to avoid deadlock.

### 20] Mutual Exclusion:

In this, if one user is using shared variable or file then other process will be excluded from doing same thing.

### 21] Semaphore:

- it is synchronization tool was developed by Dijkstra.

- Semaphore is a variable which accepts non-negative integer value and except initialization.

- It may be accessed & manipulated by two operation.

- 1) Wait
- 2) Signal.

### 22] IPC problem (classical sync<sup>n</sup> problem)

- 1) producer-consumer problem
- 2) Reader-writer problem
- 3) Dining philosopher problem.

### 23] Monitors:

- monitor is mechanism that support the safe & effective sharing of resources among process in addition to concurrency & synchronization.

- it allow control access to shared variable. It is form of data abstraction.

### 24] Deadlock:

- Deadlock is a situation, where nos of processes try access same variable at a time.

### 25] Condition for Deadlocks:

- 1) mutual exclusion
- 2) Hold & wait
- 3) NO preemption
- 4) circular wait.



## 26] Deadlock Avoidance :-

There are two approaches :-

- 1) Do not start process if its demand might lead to deadlock.
  - 2) Do not grant incremental resources request to process if this allocation might lead to deadlock.
- e.g. Bankers algorithm.

## 27] Banker Algorithms :-

already mentioned in your file.

NOTE :- UNIT-4 notes are completed.  
you should prepare all question for unit 4.  
you should prepare all example e.g.  
scheduling & Banker algorithm.

## UNIT-V

### MEMORY MANAGEMENT

#### 1] Categories of 80386DX register :-

- 1) General Purpose reg
- 2) Segment reg.
- 3) index, pointer & base reg.
- 4) Flag reg.
- 5) System Address reg.
- 6) Control register
- 7) Debug registers.

#### 2] Memory Management Techniques :-

- multiprog with fixed position
- multiprog with dynamic position

#### 3] Placement Algorithm :-

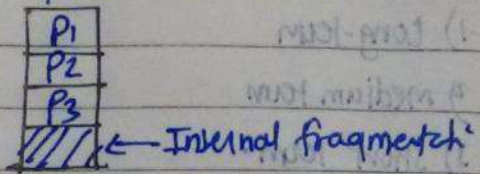
Strategies to allocate free position to program :-

- 1) First fit
- 2) Best fit
- 3) Worst fit

#### 4] Internal fragmentation :-

- In this, it occurs fixed mem allo' tech. if wastage mem. at the end of prog/mem. then internal fragmentation occurs.

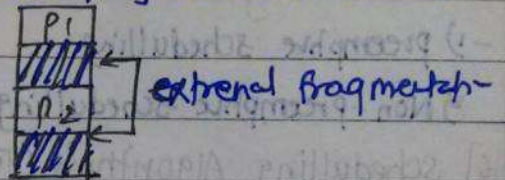
e.g.



#### 5] External fragmentation :-

- In this, Dynamic mem allocation is referred. if wastage of small chunk of mem. in throughout the memory then external fragmentation occurs.

e.g.



#### 6] Swapping :-

- moving processes from main mem to disk and back is called as swapping.

#### 7] Virtual Memory :-

- it is mem tech. in which if physical memory is not sufficient to execute prog. the virtual mem. swapped the pages to execute the program.

#### 8] Paging :-

- paging is mem management tech. that permits a prog. mem. to be non-contiguous into physical memory. This allow prog. to be allocated physical memory wherever it is possible.



## 9] Demand Paging :-

- It means that each page of program is brought in only when it is needed.
- When process is started, if there are page fault / not sufficient memory then pages are demand to OS for exe.

## 10] Page Replacement Policies :-

- First In First Out (FIFO)
- Least Recently Used (LRU)
- Optimal (OPT)
- Not Recently Used (NRU)

## 11] Design issue for paging

- 1) The working set
  - 2) Local vs Global allocation
- 2) Page Size.

## 12] Segmentation :-

- Segmentation means dividing / partitioning available memory into different partitions.

## 13] Thrashing +

- This situation may arise in demand paging when there are too many active processes in the memory and a very few pieces of any process is in memory.
- When OS bring in page in a memory it swap out another page. If OS throw out a page just before it is about to be used. Too much of this lead to condition known as thrashing.

OR

when OS demand or swapped pages for exe. & if pages are not available at that memory then we can say thrashing.

# UNIT-V

## INPUT AND OUTPUT, FILE SYSTEM

### 1] Types of I/O devices

- 1) Block devices - Disk, HDD
- 2) Character devices - KBD, printer, terminal

### 2] Techniques of DMA (Data Transfer) mode

- 1) programmed I/P / O/P
- 2) Interrupt driven I/P / O/P
- 3) Direct Memory Access.

### 3] Types of Interrupt :-

- 1) program interrupt (S/W interrupt)
- 2) Timer Interrupt
- 3) I/O Interrupt
- 4) Hardware failure.

### 3] DMA (Direct Memory Access) +

- In DMA, there is less intervention of CPU, or no intervention of CPU. If CD-ROM or other external devices try to interact with system then DMA allow these devices to directly access memory w/o using CPU.

### 4] I/O software layers :-

- 1) user processes.
- 2) Device Independent S/W
- 3) Device driver
- 4) Interrupt handler
- 5) Hardware.

### 5] Magnetic Disk :-

- It is used to store data platter, sector, track, latency etc.

PROF. ANAND GHARU

Prepared By - Prof. Anand Ghary.



6] RAID (Redundant Array of Inexpensive Disks) 7 levels:

- 1) Non-redundant
- 2) Mirrored
- 3) Redundancy thro: hamming code.
- 4) Bit-Interleaved parity.
- 5) Block level parity.
- 6) Block level Distributed parity
- 7) Dual Redundancy.

7] Disk scheduling algorithms:-

- 1) First come first served scheduling (FCFS)
- 2) Shortest Seek time First (SSTF)
- 3) Scan scheduling.
- 4) Circular Scan (C-SCAN)

8] file operation:-

- 1) creating
- 2) reading
- 3) writing
- 4) opening
- 5) closing
- 6) Renaming
- 7) Appending data to file
- 8) setting attribute.
- 9) getting attribute.

9] Types of file:-

- 1) Regular file
- 2) Directories
- 3) character special files
- 4) Block special files.

10] file Access Methods:-

- 1) The file
- 2) sequential file
- 3) Indexed file
- 4) Hashed (Direct) file.
- 5) Indexed sequential file.

11] Types of Directories:-

- 1) Flat directory
- 2) Hierarchical directory

12] Types of path:-

- 1) Absolute path
- 2) Relative path.

13] Tech of alloc of disk Blocks:-

- 1) Contiguous allocation.
- 2) Linked allocation
- 3) Indexed allocation.

14] Method of Disk management

- 1) Linked list
- 2) Bit map.

NOTE:-

UNIT-6 notes are completed even you should prepare all questions of Unit-6.

\* How to run lex & yacc program

\* How to run java code & C-code.

\* long form GCC compiler

\* lex.yy.c - what is used of it.

\* y.tab.h - what is use of it.

\* long form atoi

\* which stw is used for java.

\* How to install MS Visual studio (VS)

\* Lex & Yacc tool for window & Ubuntu.

\* -d - what is use of -d option.

\* -ll - what is use of -ll parameter.

\* stdio.h -> Long form

\*

\*\*\* BEST OF LUCK \*\*\*